L Number	Hits	Search Text	DB	Time stamp
1	278	((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:25
2	23	(((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/156.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:25
4	9441	((sram or (static adj6 random adj5 access adj5 memory)) and "memory device") and transistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:26
5	3	(((sram or (static adj6 random adj5 access adj5 memory)) and "memory device") and transistor) and ("four$1transistor" or 4T$1SRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:26
6	83	(((sram or (static adj5 random adj5 access adj5 memory)) and "memory device") and transistor) and (((((sram or (static adj5 random adj5 access adj6 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/$7.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:27
7	147	(((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/$7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:27
8	2	(((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/191.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:27
9	23	(((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/156.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:28
10	51	(((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/154.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:28
11	2	(((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/191.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:28
-	47057	sram or (static adj5 random adj5 access adj5 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:46
-	19882	(sram or (static adj5 random adj5 access adj5 memory)) and transistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:46
-	258	((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:25

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-	21	(((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/156.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:25
-	16318	(sram or (static adj5 random adj5 access adj5 memory)) and "memory device"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:26
-	8656	((sram or (static adj5 random adj5 access adj5 memory)) and "memory device") and transistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:26
-	3	(((sram or (static adj5 random adj5 access adj5 memory)) and "memory device") and transistor) and ("four$1transistor" or 4T$1SRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:26
-	3	(((sram or (static adj5 random adj5 access adj5 memory)) and "memory device") and transistor) and 4T$1SRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:54
-	75	(((sram or (static adj5 random adj5 access adj5 memory)) and "memory device") and transistor) and (((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/$7.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:27
-	0	chou-chung.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:55
-	29	chou-chung-$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:55
-	2	chung-chou.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:55
-	0	chung-chou-$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:55
-	0	chou-chung-c.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:55
-	12	"four$1transistor" or 4T$1SRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:57
-	137	(((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/$7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:27
-	2	(((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/191.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:27

-	4	(((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/226.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/14 16:27
-	48	(((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/154.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/01 01:58